



MAS-FIN-419

BOX AF

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By: Wong Hong Chen

Date: June 27, 2005

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applic. No. : 10/723,906 Confirmation No. 1149
Applicant : Wolfgang Hetzel et al.
Filed : November 26, 2003
TC/A.U. : 2811
Examiner : Hung K. Vu
Title : Electronic Component Having Stacked
Semiconductor Chips in Parallel, and a Method
for Producing the Component

Docket No. : MAS-FIN-419

Customer No. : 24131

Hon. Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

PRELIMINARY AMENDMENT

S i r :

A request for continued examination has been filed. Prior to continued examination, please amend the above-identified application as follows:

Amendments to the Claims are reflected in the listing of claims which begins on page 2 of this paper.

Remarks/Arguments begin on page 10 of this paper.